

## **METHOD AND APPARATUS FOR LEVEL SHIFTING**

### **ABSTRACT OF THE DISCLOSURE**

A level-shifter architecture with high-voltage driving capability and extremely low power consumption, exploiting dynamic control of the charge on the gate electrodes of the high-voltage output transistors, is provided. The architecture can be integrated in CMOS technology and can be applied to various applications, including

**5** monolithic integration of high-voltage display driver circuits in battery-powered applications.